

Dalhousie Faculty of Engineering

COURSE OUTCOMES WORKSHEET

COURSE NUMBER AND NAME ECED2200. Digital Circuits **PROGRAM / AU** Electrical Engineering

COMPILED BY (instructor): Yuan Ma **CHECKED BY** _____, _____

COURSE OUTCOMES (a maximum of 6 - 8, which should **NOT** just be a list of topics) (Outcomes should include: a **VERB, WHAT THE STUDENT WILL LEARN**, and the **CONTEXT** in which they will apply their learning. **How the outcomes will be measured** should also be noted (assignments, project, exam, labs, field trip reports, research reports, presentations, student electronic portfolios & questionnaires, debates, log books, case studies Rubrics will probably be needed to measure some of these.) **Circle on the second page which Graduate Attributes will be measured.**

Course Outcome 1:

verb:	what the student will learn:	context of learning (where applied):
To use	Karnaugh maps and Boolean Algebra to simplify Boolean functions	for the synthesis of combinational logic circuits (3.1.1c/KB-C)

Measurement means: assignments, exams

Course Outcome 2:

verb:	what the student will learn:	context of learning (where applied):
To use	Basic logic gates and sequential elements in	building key digital circuit components such as registers and counters (3.1.2b/PA-B)

Measurement means: assignments, labs, final exam

Course Outcome 3:

verb:	what the student will learn:	context of learning (where applied):
To design	synchronous counters and finite state machines (FSM)	for the implementation of basic controllers (3.1.4b/DS-B)

Measurement means: assignments, final exam

Course Outcome 4:

verb:	what the student will learn:	context of learning (where applied):
To use	a computer simulator package and a hardware kit	for the layout, evaluation and implementation of basic logic circuits (3.1.5c/ET-C)

Measurement means: labs