

## ECED 4260 IC Design and Fabrication

### Tutorial C: Using the ModelSim-Intel FPGA Simulator with VHDL Testbenches

#### Tutorial Material:

- Download folders **Addern**, **Accumulate**, and **Display** from the course website to your work directory.
- Follow Intel Tutorial: **Using the ModelSim-Intel FPGA Simulator with VHDL/Verilog Testbenches**

#### Submissions after the tutorial:

- A summary of the function of each HDL design (\*.v or \*.vhd);
- Waveforms from each testbench project. Annotate each waveform with one or two examples.
- Implement the **Display** circuit from Part 4 on DE1-SoC board, demonstrate your circuit to TA.